ASSOCIATE
Portland Office
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EDUCATION

J.D., *cum laude*, Lewis and Clark Law School, 2013

Computer Science graduate work, University of Washington, 2007

M.S., Electrical Engineering, University of Colorado, 1996

B.S. with Honors in Electrical Engineering, University of Wyoming, 1992

ADMISSIONS

Oregon, 2013

Washington, 2018

U.S. Patent and Trademark Office, 2010 (Reg. No. 66,964)

PRACTICE AREAS

Patents

Post-Grant USPTO Proceedings

Intellectual Property Counseling

TECHNOLOGIES

Electrical & Semiconductors

Software & Internet Technology

Mobile Devices & Applications

Physics & Optics

Nanotechnology

Mechanical

Karl G. Meier

Karl's practice focuses on the preparation and prosecution of patent applications. His practice also includes client counseling and analysis regarding licensing and patentability, patent infringement, and invalidity.

Karl has extensive industry experience developing computer architectures and integrated circuit designs for applications including embedded and reconfigurable computers, networking, storage, and liquid crystal display technologies. His technical expertise focuses on computer hardware-, software-, and electrical engineering-related technologies.

Prior to attending law school, Karl worked as a computer hardware engineer at various large companies and start-ups including Hewlett-Packard, STMicroeletronics, StarGen, and Ambric. He worked at Rockwell International, Cyrix, and Microsoft Research as an intern. Karl also held research and teaching assistant positions at the University of Washington and the University of Colorado. Prior to joining Klarquist, Karl worked as a law clerk at Mentor Graphics, Stoel Rives, and Alleman Hall McCoy Russell & Tuttle.

Karl joined Klarquist as an associate in 2013.

Professional Experience

- Mentor Graphics
  Wilsonville, Oregon
  Legal Intern, 2013
  Worked with in-house counsel to prosecute patent applications and to research and draft legal memoranda.
- Stoel Rives
  Portland, Oregon
  Summer Associate, 2012
  Prepared and prosecuted patent applications. Researched and drafted legal memoranda for the technology and intellectual property group.
- Alleman Hall McCoy Russell & Tuttle
  Portland, Oregon
  Law Clerk & Technical Consultant, 2009 – 2011
  Evaluated, prosecuted, and prepared patents for subjects including computer software and hardware, electronic, optical, and mechanical inventions. Provided technical analysis for patent non-infringement and invalidity opinions.

Klarquist

- Ambric
  Portland, Oregon
  Computer Hardware Engineer, 2007 – 2008
  Architected and designed computer hardware for Ambric's second generation massively parallel processor array (MPPA), a reconfigurable computing platform.
- Microsoft
  Redmond, Washington
  Graduate Intern, 2007
  Worked in the Embedded Hardware Group of Microsoft Research. Wrote a MIPS binary to Verilog (M2V) compiler from scratch in C++ for a reconfigurable processor. Full details of the compiler are available in MSR Technical Report 2007-128 which can be found at research.microsoft.com. A poster for this work was accepted to FCCM 2008.
- University of Washington
  Seattle, Washington
  Graduate Intern, 2006 – 2007
  Research assistant exploring tools and architectures for reconfigurable computing. Research focused on compilers, place and route, and low-power design. Teaching assistant for Advanced Logic Design. Taught advanced logic design principles using HDL. Labs were focused on building a video pipeline, trouble-shooting with Logic Analyzers, and using synthesis and place-and-route tools for FPGAs.
- Stargen
  Marlborough, Massachusetts
  Principal Hardware Engineer, 2000 – 2006
  Architect and developer of Stargen's switching and bridging chips. My main concentration was switching products with five patent applications coming from that work. I worked with PCI, PCI-Express (PCIe), Advanced Switching Interconnect (ASI), and StarFabric protocols.
- STMicroelectronics
  Cambridge, Massachusetts and Longmont, Colorado
  Research and Development Engineer, 1996 – 2000
  Lead RTL designer of a memory controller used on several generations of hard disk drive controller ICs. Performed back-end explorations for an embedded VLIW controller.
- Hewlett-Packard
  Fort Collins, Colorado
  Research and Development Engineer, 1995 – 1996
  Designed high-speed digital circuits and an IEEE 1149.1 Test Access Port (TAP) controller for a graphics ASIC.

Professional Activities

- Member, American Intellectual Property Law Association
- Member, Oregon Patent Law Association