

PATENT AGENT

Patrick C. Cesarano

patrick.cesarano@klarquist.com



EDUCATION

- M.S. with highest honors in Electrical Engineering, California Institute of Technology, 2004
- B.S., Electrical Engineering, University of Virginia, 2001
- B.S. with distinction in Physics, University of Virginia, 2001

BAR ADMISSIONS

- U.S. Patent and Trademark Office (Reg. No. 70,613)

YEAR JOINED FIRM

2017

PRACTICE AREAS

Patents: International and Utility

TECHNOLOGIES

Chemical
Consumer Products
Electrical & Semiconductors
Green Technology & Renewable Energy
Mechanical
Mobile Devices & Diagnostics
Mobile Devices & Applications
Nanotechnology
Physics & Optics
Software & Internet Technology

PRACTICE AREA OVERVIEW

Mr. Cesarano is a patent agent with expertise in patent prosecution specializing in highly technical applications focused on electrical engineering, wireless technology, and computer hardware and software. Mr. Cesarano has more than 15 years of industrial experience in electrical engineering designing technological applications for such clients as Lockheed Martin, NASA, and Oleson Microwave.

TECHNICAL EXPERTISE

Mr. Cesarano has extensive experience advising tech companies and government entities across several different technology areas, including electrical engineering and device physics fields, computer and information systems architecture and integration, cellular and wireless technology, RF engineering, and security systems architecture. His technical expertise primarily focuses on computer hardware-, software-, and electrical engineering-related technologies.

PRIOR PROFESSIONAL EXPERIENCE

Kenealy Vaidya, LLP

Patent Agent and Electrical Engineer | 2015 - 2017

Drafted electrical, mechanical, and electromechanically-founded technological applications. Performed application prior art clearances and achieved successes in patent prosecution resulting in allowances.

Oliff, PLC

Patent Agent and Electrical Engineer | 2012 - 2014

Prosecuted patents specializing in highly technical applications focused on electrical engineering, solid state devices, algorithms, and legacy applications (i.e., 2006 or earlier). Other responsibilities included patent drafting and litigation support.

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Cesarano Technologies, Inc.

Electrical Engineer Contractor and Consultant | 2008 - 2012

Consulted NASA and a variety of different tech companies in hardware and software design; systems architecture and integration; software modeling of systems, circuits, and components; and AutoCAD and inventor system idealization and specification.

L-3 Communications

Senior Engineer | 2004 - 2008

Co-architect and senior engineer of the Warner Robins Air Force Base JSTARS security system; designed sensor interface server (FPGA-SIS), a universal sensor monitor and XML translator; supervised subcontractors, coordinated labor, and acted as liaison to government customers and air force base personnel.

California Institute of Technology

Electrical Engineer and Research Assistant | 2002 - 2004

Designed numerous MMICs, including amplifiers, frequency multipliers, mixers, broadband filters, attenuators, and cryogenically cooled MMIC control modules; designed a broadband Q-Band amplifier and broadband dual polarization Ka-band (26-40 GHz) down converter; and designed and fabricated a unique "three-fingered" hybrid Schottky diode.

Virginia Diodes, Inc. (VDI)

Technical Assistant | 2001 - 2002

Developed and tested microwave technologies spanning frequencies from 18 GHz to 3 THz for Lockheed Martin, NASA, Oleson Microwave, and others. Designed passive waveguide components and module chassis using HFSS.

PROFESSIONAL ACTIVITIES

- Former Member, Institute of Electrical and Electronics Engineers (IEEE)

PRESENTATIONS AND PUBLICATIONS

- "A Millimeter-Wave Diode-MMIC Chipset for Local Oscillator Generation in the ALMA Telescope," *Microwave Symposium Digest*, 2005 IEEE MTT-S International (2005)
- "An Introduction to Modern Semiconductor Lasers," *University of Virginia Press* (2002)